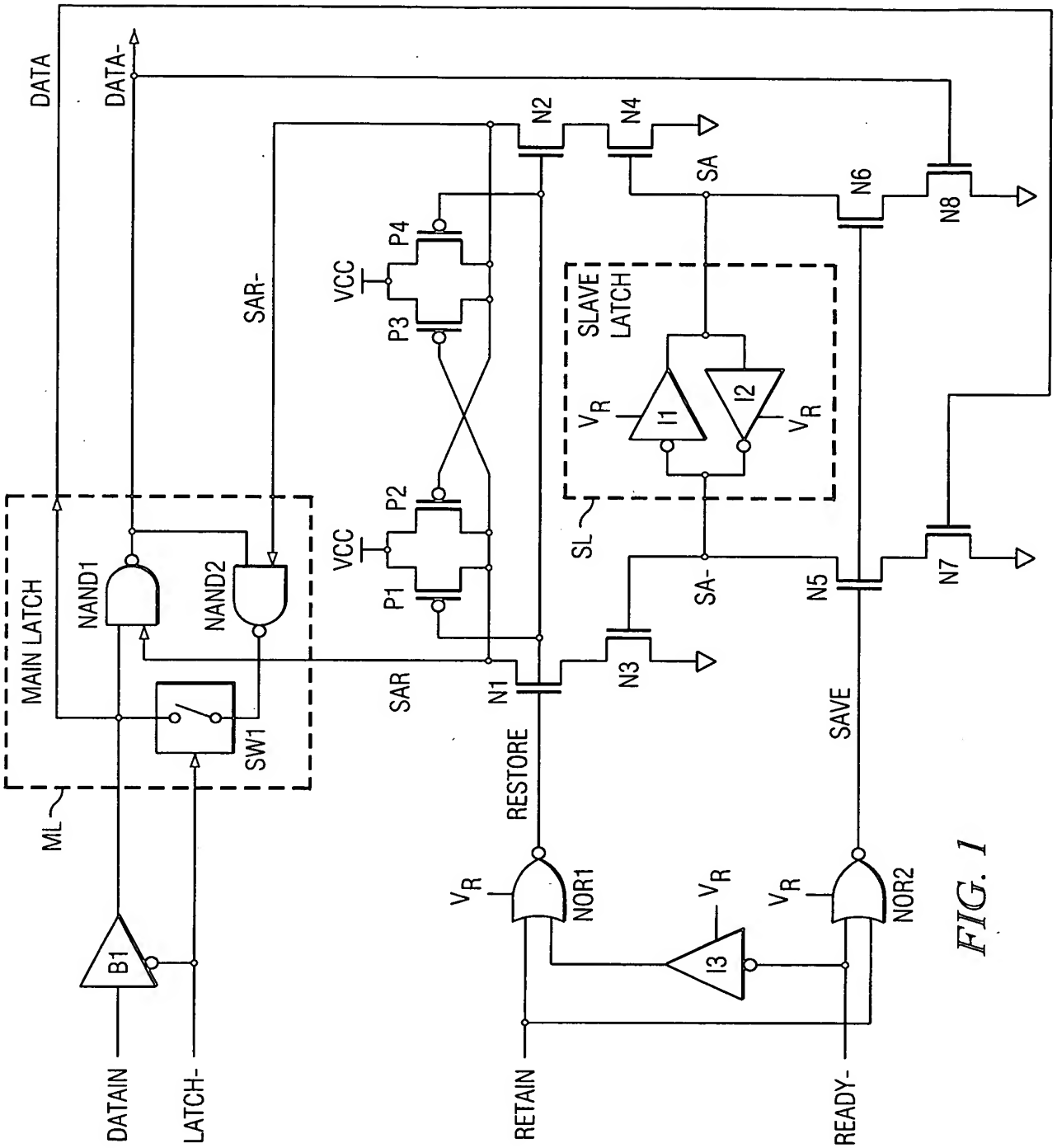
DATA
DATA-
SAR-
N2
N4
SA
N6
N8
P4
VCC
P3
SLAVE LATCH
V_R
I1
I2
V_R
P2
VCC
P1
SL
SA-
N5
N7
MAIN LATCH
NAND1
NAND2
SAR
N1
N3
SAVE
SW1
ML
RESTORE
V_R
NOR1
V_R
V_R
NOR2
B1
I3
DATAIN
LATCH-
RETAIN
READY-

FIG. 1

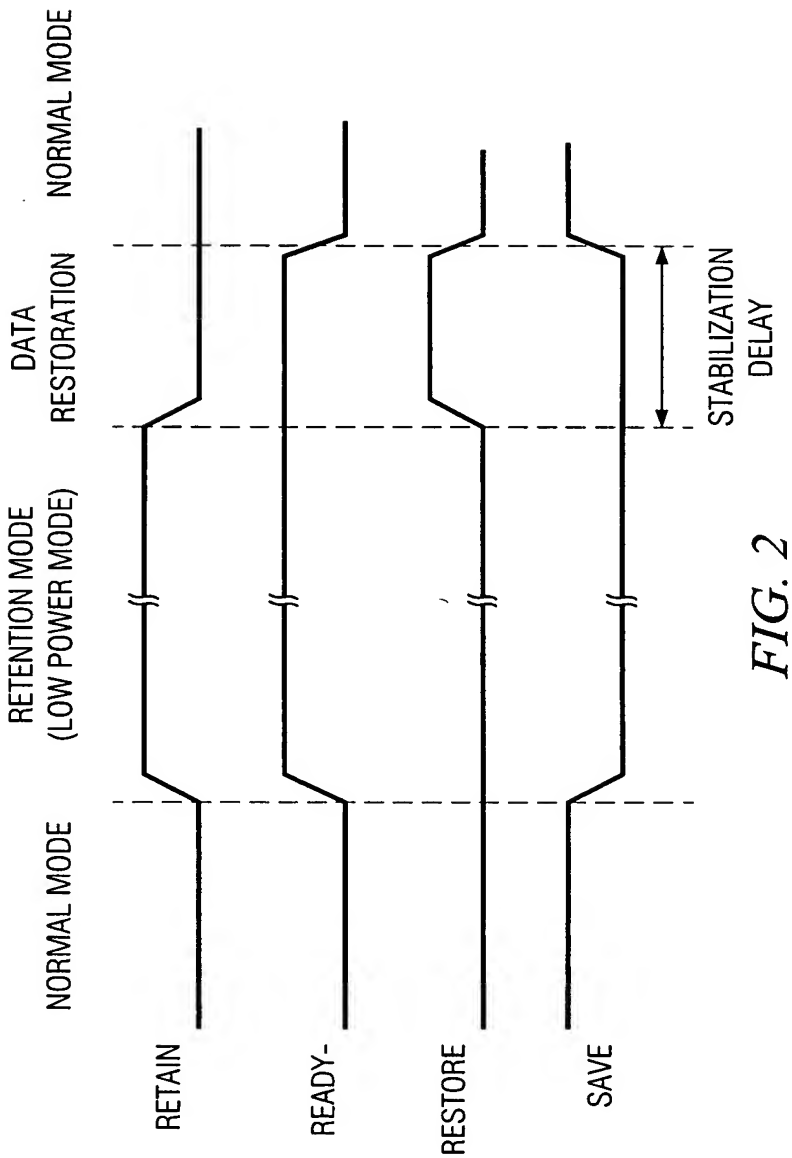
NORMAL MODE
DATA RESTORATION
NORMAL MODE
RETENTION MODE (LOW POWER MODE)
STABILIZATION DELAY
RETAIN
READY-
RESTORE
SAVE

FIG. 2